●General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

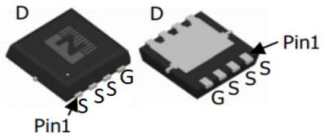
●Features

- ■Low $R_{DS(ON)}$ to minimize conductive loss
- ■Low Gate Charge for fast switching
- ■Low thermal resistance
- ■AEC-Q101 qualified

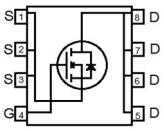
●Application

- ■BLDC motor driver
- ■DC-DC
- ■Load switch

●Product Summary



DFN3*3



V_{DS}=80V

$R_{DS(ON)}$ =12mR

I_D=48A

RoHS REACH COMPLIANT HF

●Ordering Information

Part NO.	ZMSA120N08M
Marking	120N08
Packing information	REEL TAPE
Basic ordering unit (pcs)	5000

●Absolute Maximum Ratings（T_A=25℃,unless otherwise specified）

Parameter	Symbol	Conditions	Min.	Max.	Unit
Drain-source voltage	V_{DS}	25℃≤Tj≤175℃	-	80	V
Gate-source voltage①	V_{GS}	DC; -55℃≤Tj≤175℃	-20	20	V
Continuous drain current	I_D	V_{GS}=10V, T_C=25℃	-	48	A
	I_D	V_{GS}=10V, T_C=75℃	-	40	A
	I_D	V_{GS}=10V, T_C=100℃	-	34	A
Pulsed drain current	I_{DM}	Pulsed; t_p ≤ 10 μs; T_C = 25 °C;	-	162	A
Diode continuous current	I_S	V_{GS}=0V, T_C=25℃	-	48	A
Diode pulse current	$I_{S,pulse}$	Pulsed; t_p ≤ 10 μs; T_C = 25 °C;	-	162	A
Total power dissipation	P_D	V_{GS}＞6V, T_C=25℃	-	75	W
Total power dissipation	P_D	V_{GS}＞6V, T_A=25℃	-	2.5	W
Operating junction temperature	T_J		-55	175	℃
Storage temperature	T_{STG}		-55	175	℃
Single pulse avalanche energy	E_{AS}	L=0.1mH, V_{GS}=10V, R_g=25Ω	-	23	mJ

Single pulse avalanche energy	E_{AS}	L=0.5mH, V_{GS}=10V, R_g=25Ω	-	44	mJ
ESD level (HBM)			CLASS 1B		

●Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	2	°C/W
Thermal resistance, junction - ambient	R_{thJA}②	-	-	60	°C/W
Soldering temperature	T_{sold}	-	-	260	°C

●Electronic Characteristics (T_j=25°C, unless otherwise specified)

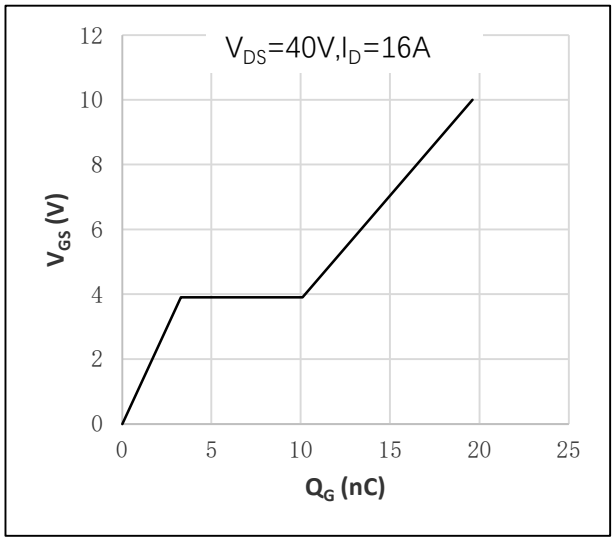
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	V_{GS} =0V, I_D =250uA, T_j=25℃	80	-	-	V
		V_{GS} =0V, I_D =250uA, T_j=-55℃	76.4	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_D=250uA, T_j=25℃	1.3	1.8	2.5	V
		V_{GS}=V_{DS}, I_D=1mA, T_j=-55℃	-	-	3	V
		V_{GS}=V_{DS}, I_D=1mA, T_j=175℃	0.7	-	-	V
Drain-source leakage current	I_{DSS}	V_{GS}=0V, V_{DS}=80V, T_j=25℃	-	-	1	uA
		V_{GS}=0V, V_{DS}=80V, T_j=175℃	-	-	500	uA
Gate- source leakage current	I_{GSS}	V_{GS}=±20V, V_{DS} =0V	-	-	100	nA
Static drain-source on resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=16A, T_j=25℃	-	12	16	mΩ
		V_{GS}=10V, I_D=16A, T_j=175℃	-	24.1	-	mΩ
		V_{GS}=4.5V, I_D=12A, T_j=25℃	-	18	23	mΩ
Forward transconductance	g_{FS}	V_{DS} =5V, I_{SD} = 10A	-	10	-	S
Diode forward voltage	V_{FSD}	V_{GS} =0V, I_{SD} = 16A	-	0.9	1.3	V

●Dynamic characteristics (T_j=25°C, unless otherwise specified)

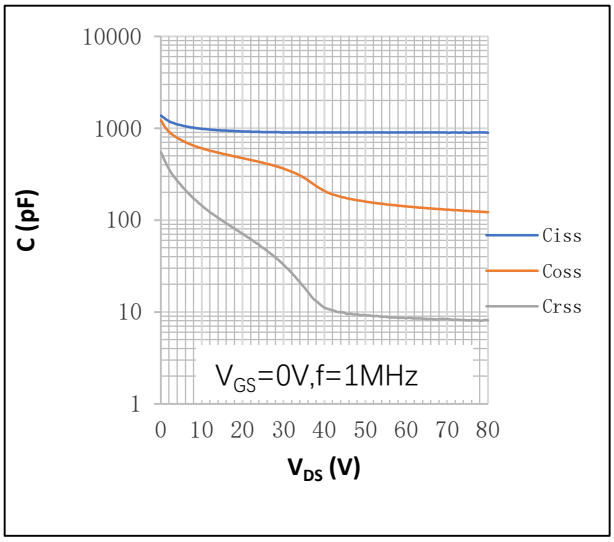
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	f = 1MHz, V_{DS}=40V, V_{GS}=0V	-	899	-	pF
Output capacitance	C_{oss}		-	204	-	pF
Reverse transfer capacitance	C_{rss}		-	11	-	pF
Gate resistance	R_g	f = 1MHz	-	1.4	-	Ω
Total gate charge	Q_g	V_{DD} = 40V, I_D = 16A, V_{GS} = 10V	-	19.6	-	nC
Total gate charge	$Q_{g(4.5V)}$		-	11	-	nC
Gate-source charge	Q_{gs}		-	3.3	-	nC
Gate-drain charge	Q_{gd}		-	6.8	-	nC

Turn-on delay time	$t_{D(on)}$	V_{GS}=10V, V_{DS}=40V, R_G =3.3Ω, I_D =16A	-	16	-	ns
Turn-on rise time	t_r		-	40	-	ns
Turn-off delay time	$t_{D(off)}$		-	20	-	ns
Turn-off fall time	t_f		-	12	-	ns
Reverse recovery time	t_{rr}	V_{DD}=40V, dI_S/dt = 100A/us, I_S=16A	-	45	-	ns
Reverse recovery charge	Q_{rr}		-	40	-	nC

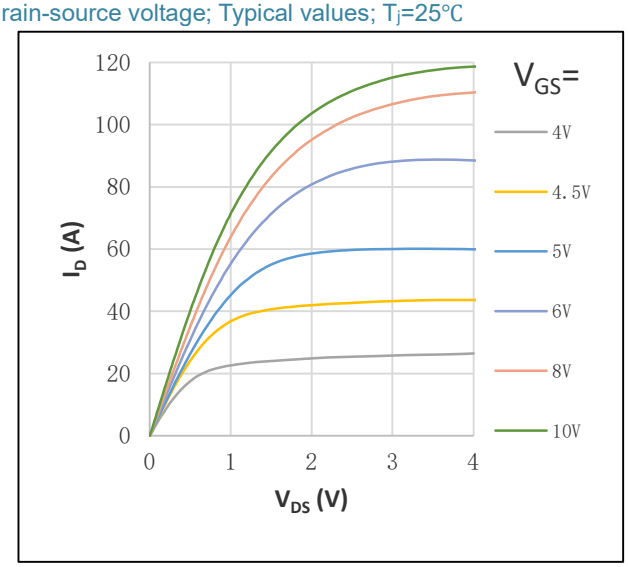
●Fig.1 Gate-source voltage as a function of gate charge; Typical values; T_j=25℃



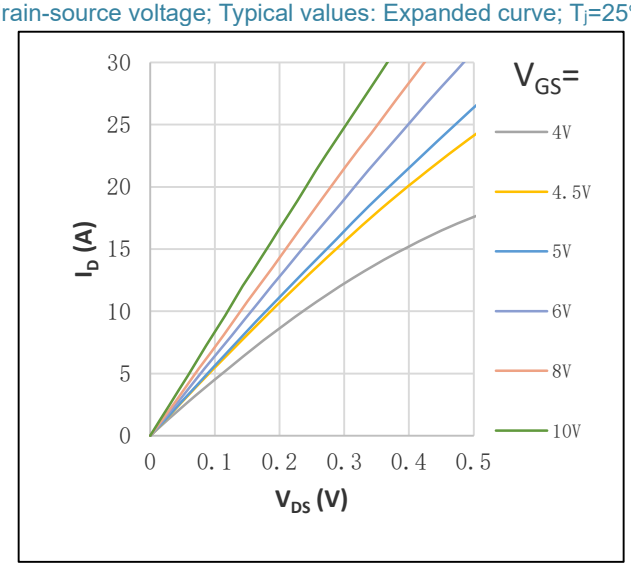
●Fig.2 Input, output and reverse transfer capacitances as a function of drain-source voltage; Typical values; T_j=25℃



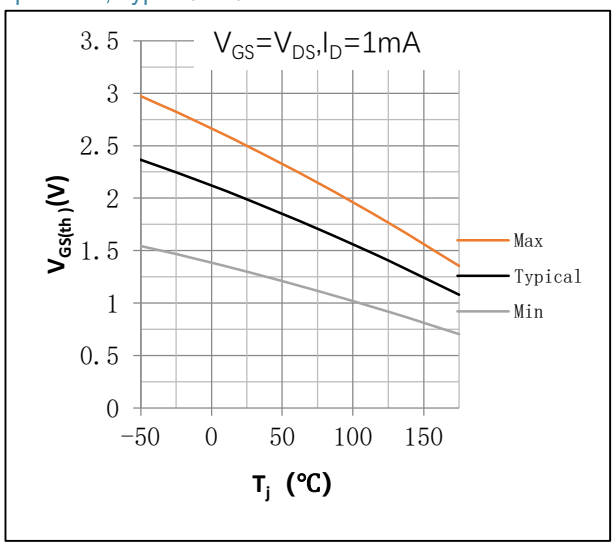
●Fig.3 Output characteristics: drain current as a function of drain-source voltage; Typical values; T_j=25℃



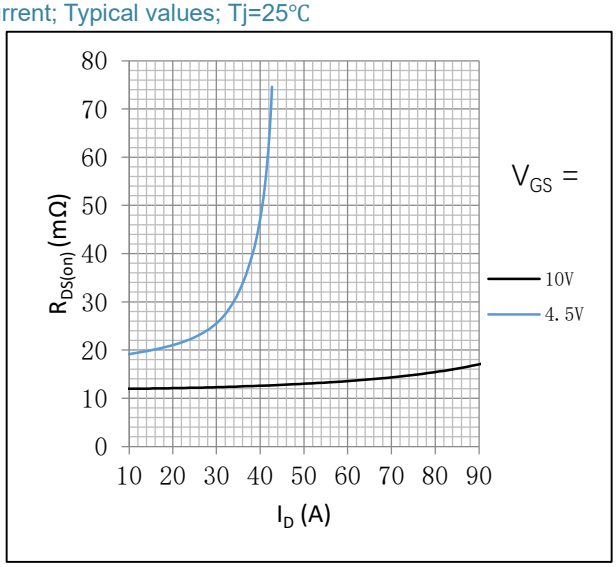
●Fig.4 Output characteristics: drain current as a function of drain-source voltage; Typical values: Expanded curve; T_j=25℃



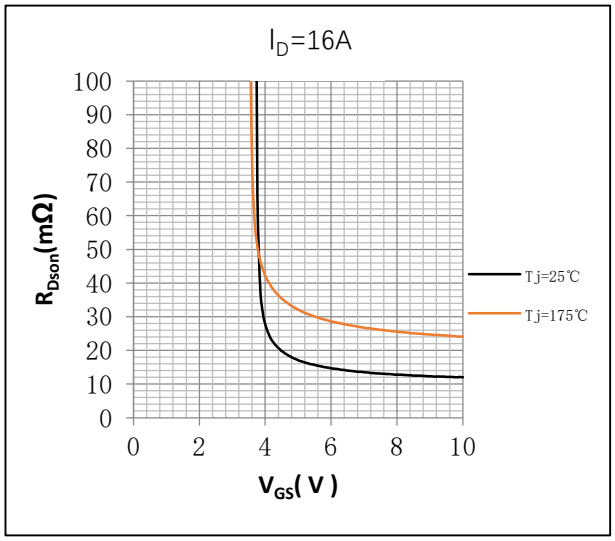
●Fig.5 Gate-source threshold voltage as a function of junction temperature; Typical/Min/Max values



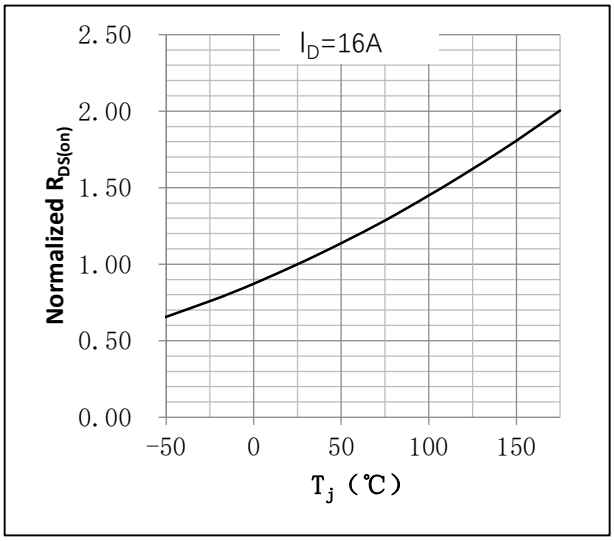
●Fig.6 Drain-source on-state resistance as a function of drain current; Typical values; Tj=25℃



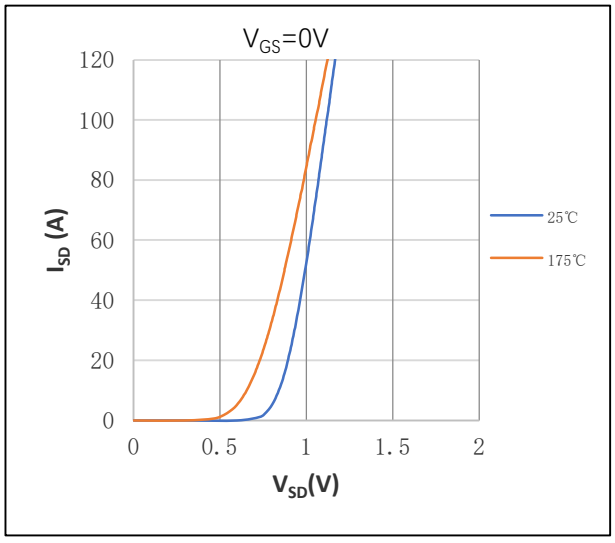
●Fig.7 Drain-source on-state resistance as a function of gate-source voltage; Typical values



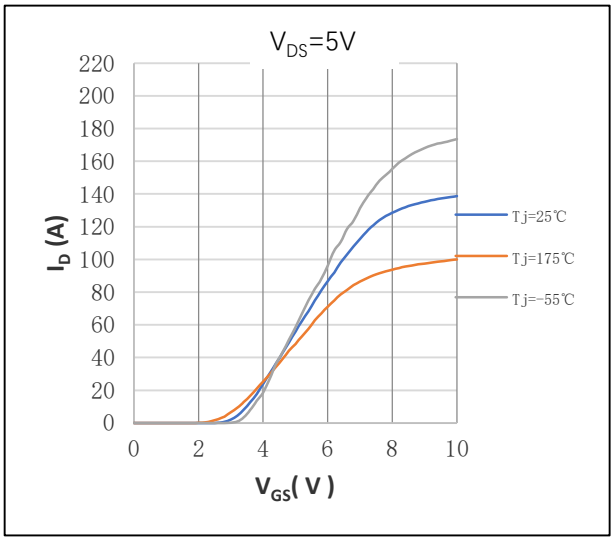
●Fig.8 Normalized drain-source on-state resistance factor as a function of junction temperature; Typical values Normalized On-Resistance=R_{DSon}/R_{DSon}(25℃)



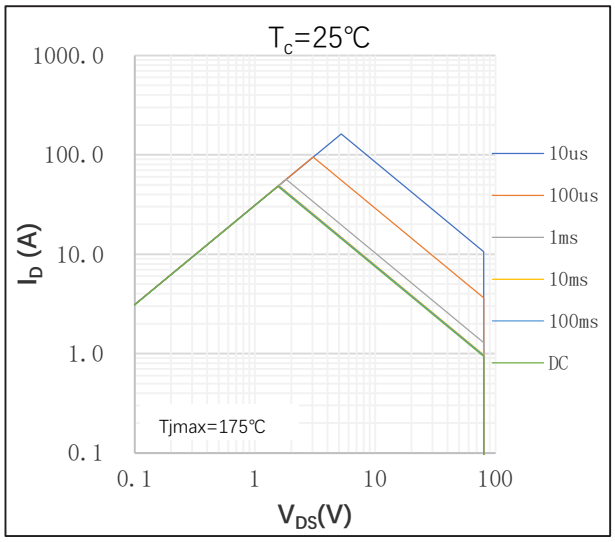
●Figure 9. Source (diode forward) current as a function of source-drain (diode forward) voltage; Typical values



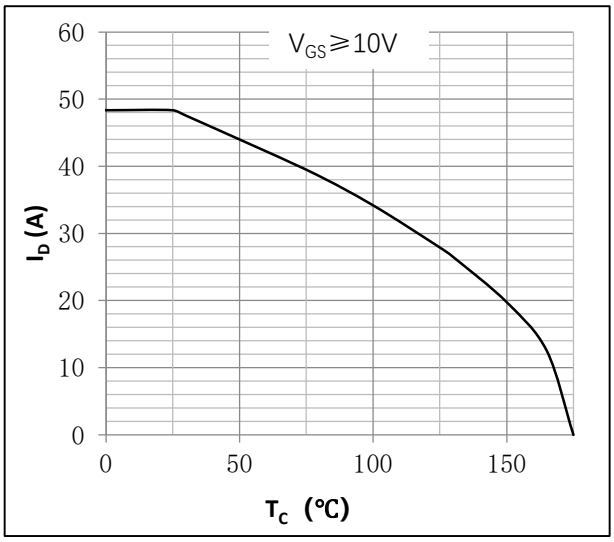
●Figure 10. Transfer characteristics: drain current as a function of gate-source voltage; Typical values



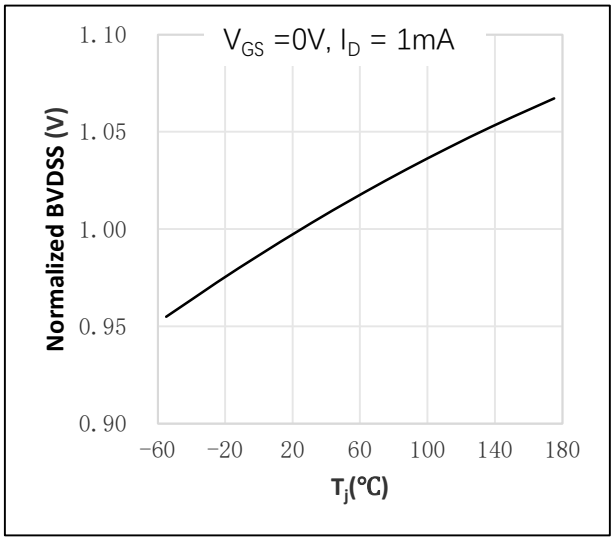
●Fig.11 Safe operating area: continuous and peak drain currents as a function of drain-source voltage; Calculative values



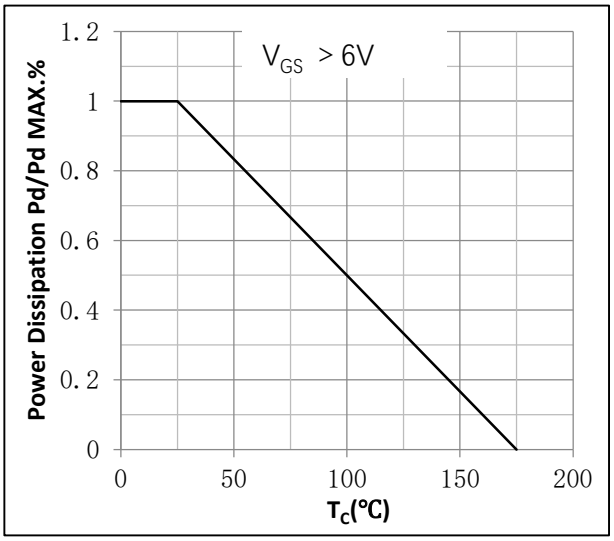
●Fig.12 Continuous drain current as a function of case temperature③;Calculative values



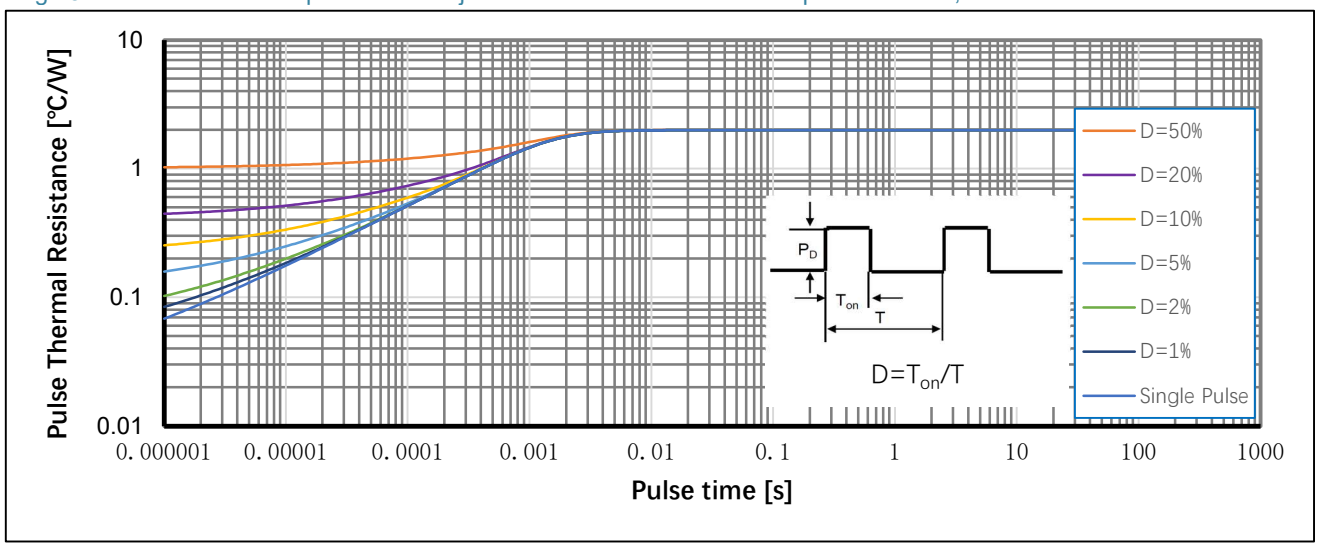
●Fig.13 Drain-source breakdown voltage as a function of junction temperature; Min values



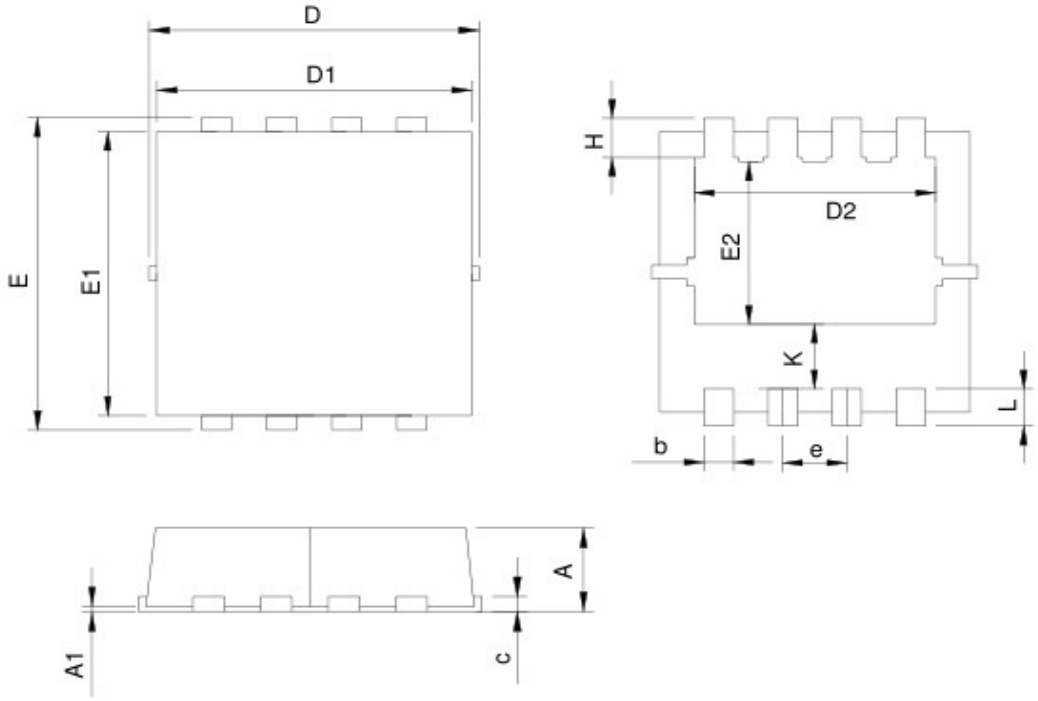
●Fig.14 Normalized total power dissipation as a function of case temperature; Calculative values Normalized Power Dissipation =P_d/P_d(25℃)



●Fig.15 Transient thermal impedance from junction to case as a function of pulse duration; max values

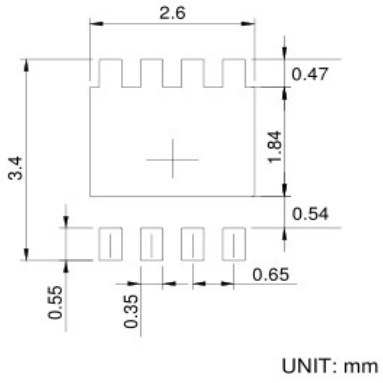


●Package Outline



SYMBOL	DFN3.3x3.3-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	1.00	0.028	0.039
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
c	0.14	0.20	0.006	0.008
D	3.10	3.50	0.122	0.138
D1	3.05	3.25	0.120	0.128
D2	2.35	2.55	0.093	0.100
E	3.10	3.50	0.122	0.138
E1	2.90	3.10	0.114	0.122
E2	1.64	1.84	0.065	0.072
e	0.65 BSC		0.026 BSC	
H	0.32	0.52	0.013	0.020
K	0.59	0.79	0.023	0.031
L	0.25	0.55	0.010	0.022

RECOMMENDED LAND PATTERN



UNIT: mm

Note:

1. Refer to JEDEC MO-240.

●Note

① Pulse : V_{GS}=+20V/-20V, Duty cycle=50%,T_j=175℃, t=1000 hours; For DC， the following test conditions can be passed: V_{GS}=+20V/-10V, T_j=175℃, t=1000 hours;

② Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;

③Practically the current will be limited by PCB, thermal design and operating temperature. V_{GS}=10V.

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●Revision History

Version	Date	Change
A	2022/2/10	New
B	2022/10/10	Add Reach, HF figure
C	2023/12/20	1.ID, Rthjc modified
C1	2025/1/3	Add JEDEC reference document number in POD
D	2025/7/24	Apply new datasheet format.
E	2026/6/4	Modify Rthjc,ID,SOA
F	2026/7/6	Update Cxss, Qg,etc.